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No. 0213

P. 2

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11/18/2005

SHREEN K. DANAMRAJ
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Shreen K. Danamraj	(Depositor's name)
<i>Shreen K. Danamraj</i>	(Signature)
February 21, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/816,763	04/02/2004	Amit Khanuja	1263-0024US	2778

TITLE OF INVENTION: PARTITIONED SOURCE LINE ARCHITECTURE FOR ROM

03/14/2006 CNGUYEN3 00000034 10816763

01 FC-2501	700.00 OP	02 FC-8004 PPPLN. TYPE	SMALL ENTITY 00 OP	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700		\$0	\$700		02/21/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
DINH, SON T	2824	365-063000

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Danamraj & Youst, P.C.
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Virage Logic Corp.

Fremont, CA

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Amit Khanuja

Date February 21, 2006

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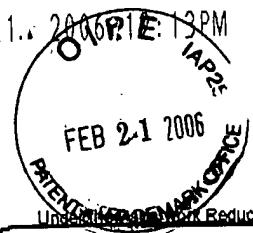
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TRANSMITTAL
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Application Number	10/816,763
Filing Date	April 2, 2004
First Named Inventor	Amit Khanuja
Art Unit	2824
Examiner Name	Son T. Dinh
Attorney Docket Number	1263-0024US

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

firm Name	Danamraj & Youst, P.C.		
Signature			
Printed name	Shreen K. Danamraj		
Date	February 21, 2006	Reg. No.	41,696

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